## N THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: William P. Ward

Serial No.: 09/651,229

Filed: August 25, 2002

For: Providing Multiple Memory

Controllers on a Memory Bus

Group Art Unit: 2186

Examiner: P.M. Bataille

Atty. Dkt. No. 9295 (NCR.0020US)

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313 RECEIVED

JUN 1 4 2004

Technology Center 2100

## REPLY TO OFFICE ACTION DATED MARCH 4, 2004

## Dear Sir:

In response to the Office Action dated March 4, 2004, please amend the aboveidentified application as follows:

The Listing of Claims begin on page 2 of this paper. The claims have not been amended by this paper.

Remarks/Arguments begin on page 9 of this paper.

Date of Deposit: 1 2004

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria,

Dawn L. Thomas